

A Monitoring Tool for a Branching-Time Logic

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Outline

Property Specification and Monitor Synthesis

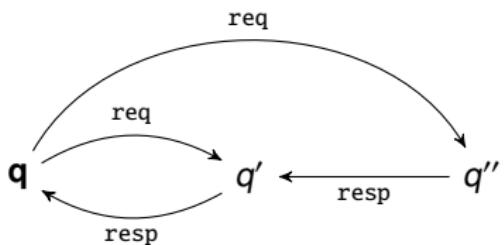
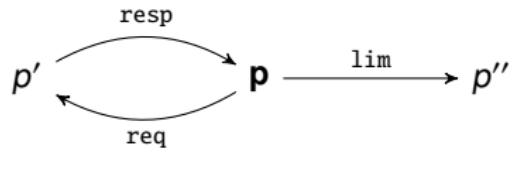
Implementation of a Tool in Erlang

Demo

Modelling Process Behaviour

The behaviour of systems can be described using LTSes that model process execution graphs

Example (Two simple servers)



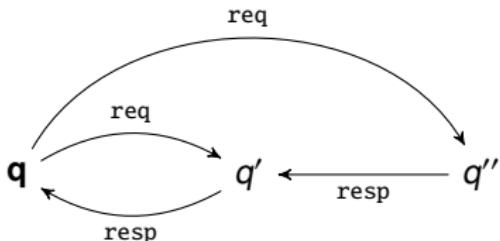
The Branching-Time Logic μHML

Syntax

$$\varphi, \phi \in \mu\text{HML} ::= \text{tt} \quad | \quad [\alpha]\varphi \quad | \quad \varphi \wedge \phi \quad | \quad \mathbf{max} \ X.\varphi \quad | \quad X \\ | \quad \mathbf{ff} \quad | \quad \langle\alpha\rangle\varphi \quad | \quad \varphi \vee \phi \quad | \quad \mathbf{min} \ X.\varphi$$

Example (A liveness property)

$[\text{req}]\langle\text{resp}\rangle\text{tt}$



Cheat sheet

$[\alpha]\mathbf{ff}$ “cannot do event α ”

$\langle\alpha\rangle\mathbf{tt}$ “can do event α ”

A Monitorable Subset

The Syntax

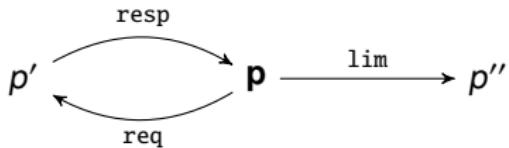
$$\psi \in \text{mHML} \stackrel{\text{def}}{=} \text{sHML} \cup \text{cHML}$$

$$\theta, \vartheta \in \text{sHML} ::= \text{tt} \quad | \quad \text{ff} \quad | \quad [\alpha]\theta \quad | \quad \theta \wedge \vartheta \quad | \quad \mathbf{max} \ X.\theta \quad | \quad X$$

$$\pi, \varpi \in \text{cHML} ::= \text{tt} \quad | \quad \text{ff} \quad | \quad \langle \alpha \rangle \pi \quad | \quad \pi \vee \varpi \quad | \quad \mathbf{min} \ X.\pi \quad | \quad X$$

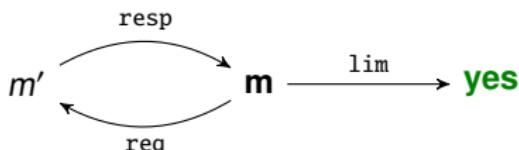
Example (A co-safety property)

$$\mathbf{min} \ X. (\langle \text{req} \rangle \langle \text{resp} \rangle X \vee \langle \text{lim} \rangle \text{tt})$$



Monitors as LTSeS

Example (A monitor for process p)



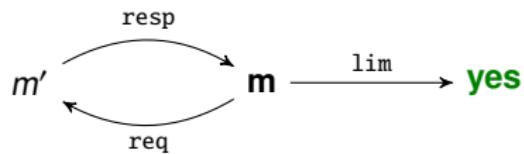
- ▶ A **subtle** difference: processes *generate* actions, monitors *analyse* these actions, and yield verdicts
- ▶ Conclusive verdicts: **no**, **yes**
- ▶ Inconclusive verdict: **end**

Monitor Synthesis

From mHML to Monitors

Correct Synthesis (Compositional)

$\min X.(\langle \text{req} \rangle \langle \text{resp} \rangle X \vee \langle \text{lim} \rangle \text{tt})$



Semantics

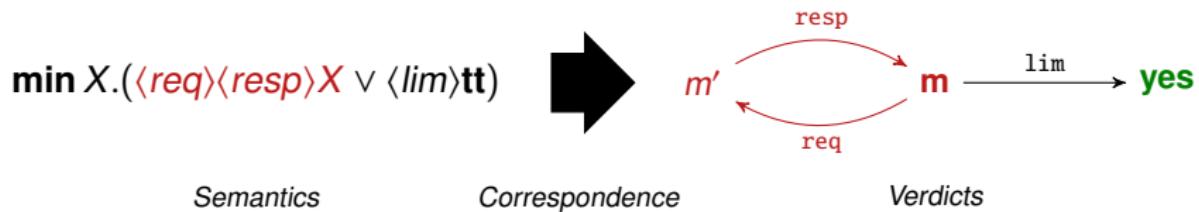
Correspondence

Verdicts

Monitor Synthesis

From mHML to Monitors

Correct Synthesis (Compositional)

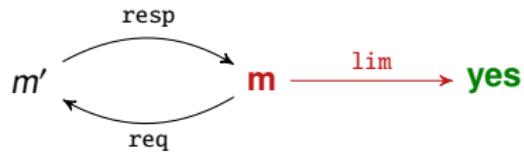


Monitor Synthesis

From mHML to Monitors

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Semantics

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Actions = Events with Data

- ▶ Why? Because we want to look at the *data* inside actions
- ▶ Events with structure permit us to use pattern matching
- ▶ Output events: $\alpha = \text{client} ! \{\text{resp}, 5\}$
- ▶ Input events: $\alpha = \text{server} ? \{\text{req}, \text{client}, 5\}$

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Example (Pattern variable binding)

A successor server *Srv* adds one to any numeric payload it receives from clients *Clt*

$$[\text{Srv} ? \{\text{req}, \text{Clt}, \text{Num}\}] [\text{Clt} ! \{\text{resp}, \text{Num}\}] \mathbf{ff}$$

Safety formula ensures that clients *do not* receive the same value *Num* sent by them to the server

The Tool Synthesis

Example (Non-recursive formula)

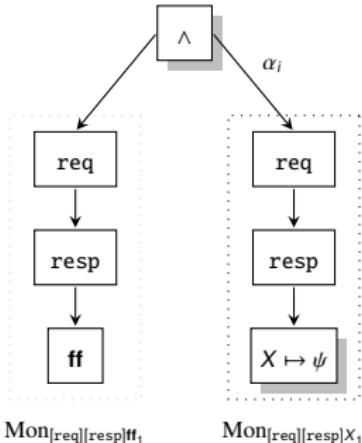
$[Srv ? \{req, Clt, Num\}][Clt ! \{resp, Num\}]ff$

- ▶ Non-recursive formulae can only observe one interaction before terminating

The Tool Synthesis

Example (Recursive formula)

```
max X.(  
  [Srv ? {req, Clt, Num}][Clt ! {resp, Num}]ff  
  ^  
  [Srv ? {req, Clt, Num}][Clt ! {resp, Succ}]X  
)
```

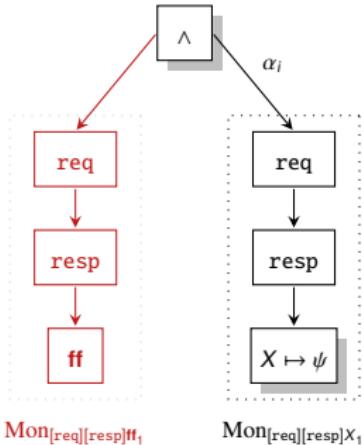


- ▶ Recursive formulae allow continuous monitoring
- ▶ The verdict branch matches events that lead to a detection
- ▶ The recursive branch permits the monitor to unfold *lazily*

The Tool Synthesis

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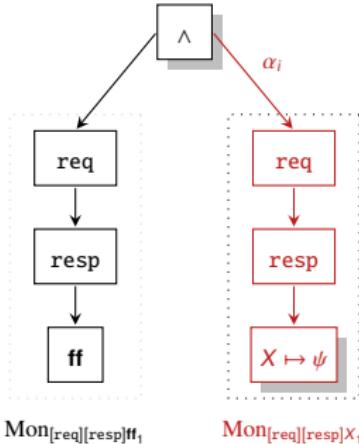


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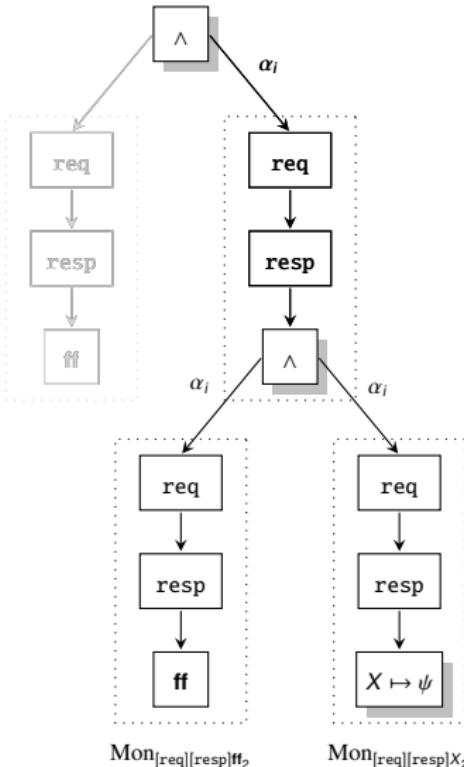
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Formal vs. Tool Synthesis

$[Srv ? \{req, Clt, Num\}][Clt ! \{resp, Num\}]ff$

```
formula:mon_nec(
    fun(Action) ->
        case Action of
            {recv, Srv, {req, Clt, Num}} ->
                formula:mon_nec(
                    fun(Action) ->
                        case Action of
                            {send, Clt, {resp, Num}} -> formula:mon_ff();
                            _ -> formula:mon_id()
                        end
                    end);
            _ -> formula:mon_id()
    end
end)
```

Formal vs. Tool Synthesis

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                    end);
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Formal vs. Tool Synthesis

[*Srv* ? {req, *Clt*, *Num*}][*Clt* ! {resp, *Num*}]**ff**

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Thank You!

Resources

The tool can be downloaded from:

<https://bitbucket.org/duncanatt/detecter-lite>

Information and publications from:

<http://www.cs.um.edu.mt/svrg/Tools/detectEr>

Questions?