Runtime Instrumentation for Reactive Components

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Reactive systems



 st Understanding system behaviour requires the system to run $_{
m ss}$



resource usage analysis

security audit trails

OR

 $^{
m eta}$ Correctness of system is hard to analyse statically $_{
m eta}$

(debugging)

information flow

runtime verification

Runtime monitoring...How?

Extract runtime information and report it to monitors ,



Runtime monitoring requirements

 $^{
m \ref{sol}}$ Instrumentation **must** preserve the reactiveness of the system $_{
m sol}$

Runtime monitoring requirements

Instrumentation must preserve the reactiveness of the system ,

Low overhead preserves the (Responsive) attribute

Independent failure preserves the Resilient attribute

Non-blocking preserves the Message-driven attribute

Grows and shrinks preserves the Elastic attribute

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m ss}$









Trace soundness

- Complete: trace contains all the events exhibited by P so far
- Consistent: events reflect the same order P exhibits them



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Centralised outline instrumentation



Not scalable due to contention and singleton monitor

Requires demultiplexing for analysing events

Inline instrumentation



Inapplicable when code modification is not possible

Slow monitors may **impact latency**

Decentralised outline instrumentation





Tracing uses an asynchronous tracing infrastructure

Dynamic outline instrumentation = **challenging** engineering

Criticism against decentralised outline monitoring

Decentralised outline monitoring induces high overhead ,

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What is runtime overhead?

Many take the **execution duration** as an overhead metric

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(Memory consumption) + (Scheduler usage)

sage) 🗸

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RIARC

⁶⁶ A *reactive* decentralised outline instrumentation algorithm ₉₉

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⁶⁶ Buffers react to key trace events to reorganise monitors

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Trace events

- spawn (-<>), exit (★)
- send (!), receive (?)

Control messages

- route packet (rtd)
- detach request (dtc)

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(Implementability)

To confirm that RIARC can be used in **practice**

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Implementability

To confirm that RIARC can be used in **practice**



To confirm that all traces RIARC reports are **sound**

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To confirm that all traces RIARC reports are **sound**

Performance

To confirm that RIARC preserves the system reactiveness

Limited hardware - 40 M events

High concurrency experiments

(short-lived, computationally-light tasks)

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Commodity hardware - 200 M events

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Centralised is **impractical**

Inline is the **most efficient**

Latency RIARC \approx latency Inline

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Inline prone to **slow analysis**

Instrumentation yields **most** overhead %



Centralised instrumentation

Applicable 🗡

Inline instrumentation

Low overhead \checkmark

May impact system 🗡

Not always applicable X

RIARC

- Guarantees trace soundness ✓
- Low overhead feasible for soft real-time applications ✓

Further details



Paper (extended version)



RIARC use in detectEr





Resource usage

(complete system runs)













Resource usage (complete system runs)





